

Paragraph on page 7, lines 17-18

A2 FIGS. 20A-20B are a diagram of an array of 6 levels of 4:2 compressors,
according to an embodiment;

Paragraph on page 7, line 21

A3 FIGS. 22A-22B are a diagram of vector $d_{w/v}$ computations, according to an
embodiment; and

Paragraph on page 43, lines 10-12

A4 FIGS. 8A-8B are a flow diagram that depicts the control flow for modular
multiplication in RNS, according to an embodiment. In FIGS. 8A-8B, rectangular blocks
on the same horizontal level overlap execution times.

Paragraph on page 51, lines 22-23

A5 FIGS. 20A-20B are a diagram of an array of 6 levels of 4:2 compressors organized
into six levels, according to an embodiment.

Paragraph on page 52 line 3

A6 FIGS. 22A-22B are a diagram of vector $d_{w/v}$ computations, according to an
embodiment.